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6.002 is designed to serve
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subjects required for all
undergraduates in EECS. The
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fundamentals of the lumped
circuit abstraction.

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voltage, V_{DS} , for the
following circuits and
specifications Q_2 R_2 200 Ω
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the gate and drain are
connected by a short circuit
which gives $V_{GS} = V_{DS}$. The
FET cannot be in the Triode
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From the course home page:

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