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2 (Simulation, Analysis and calculator use)
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~~Complex Variables, Lec 3: Conformal Mappings~~
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ModApkTown At CDNLive Korea, Cadence announced the logical equivalence checking (LEC) would get smart. And not in the Maxwell

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Smart sense. It's about 20 times faster and doesn't require manual guidance (self-driving LEC). The new product is Cadence Conformal Lec User Guide - e13components.com

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user and can scale seamlessly to 100+ CPUs. Cadence® Conformal Smart LEC is the next-generation equivalence checking solution. Offering key technologies of massive parallelism and adaptive proof, Conformal Smart LEC improves runtime by an average of 4X compared to existing solutions with the same compute resources. Designers can

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Conformal Smart LEC - Cadence Design Systems
Conformal Smart Logic Equivalence Checker is the next-generation equivalency checking solution. With massively parallel architecture and adaptive proof technology, the Conformal Smart LEC delivers dramatic turnaround time improvements in equivalence checking by over 20X for RTL-to-gate comparisons.

Conformal Smart LEC - Cadence Design Systems
Cadence conformal – Question on Formal checking in Verification. Matching the

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Revised key points to the Golden:. Specify the name of the log file where LEC session transcript is to be buide. Conformal LEC constant constraint. This is the default when running the lec command with no. The lec command has the following additional options.

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Equivalence Checking Using Cadence Conformal LEC

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Conformal ECOs at Marvell during pre-mask stage. We have done multiple ECOs using Conformal automated flow on various projects for the past few years. Below is the data for

- Example 1 – Block A – Pre-mask ECO was done inside a control block which was instanced multiple(12) times. ECO fix involved combinational logic change in control block.

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Practical ECOs using Conformal ECO tool - Cadence

Cadence® Conformal® Equivalence Checker (EC) makes it possible to verify and debug multi-million-gate designs without using test vectors. It offers the industry's only complete equivalence checking solution for verifying SoC designs—from RTL to final LVS netlist (SPICE)—as well as FPGA designs.

Conformal Equivalence Checker - Cadence Design Systems

Cadence Conformal suite of tools contains a

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tool called Logic Equivalence Checker or LEC. Section 4 of this tutorial describes how to formally verify that the synthesized design is functionally equivalent to the RTL description using LEC. This section can be skipped if one chooses to use Formality for equivalence checking.

Digital Logic Synthesis and Equivalence Checking Tools ...

LEC comprises of three steps as shown below: Setup Mode, Mapping Mode and Compare Mode.

Fig-1. Logical Equivalence Check flow diagram. There are various EDA tools for

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performing LEC, such as Synopsys Formality and Cadence Conformal. We are considering Conformal tool as a reference for the purpose of explaining the importance of LEC.

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