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10. a) b) 11. Change the
AND gates to NAND
gates. The rest of the
circuit is unchanged. 12.
Remove the tri-state
buffers and do one of
the following: a)
Change each 2-input
AND gate to a 3-input
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a. Say Program P1 consists of n x86 instructions, and hence $1.5 \times n$ MIPS instructions. Computer A operates at 2.5 GHz,

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