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 at all. But digital design with VHDL stumped them: they did not have any real experience designing digital circuits, despite having taken a beginning digital design class. They were also ummox ed by the odd syntax of VHDL. Many of them resorted to trying to write VHDL as if it were C. In the spring of 2005, I made the labs more hardware-centric.

Experiences Teaching an FPGA-based Embedded Systems Class
 Digital Electronics: A Practical Approach with VHDL William Kleitz 2011 978-0-13-254303-3 Course Outcomes: By the end of the course students are able to: 1. Derive, simplify, and solve Boolean algebra expressions 2. Analyze and design simple combinational logic circuits 3. Analyze and incorporate functional components such as

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