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introduction chapter 1

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~~Combinational Logic Circuits, Functions,
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Systems/Electronics Digital System

Design Capstone, CSE477, Spring 2013

Digital System Design Inside your

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~~Interview Question: DESIGN A~~

~~PARKING LOT~~ asked at Google,

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Preparation (SIP) System Design

Interview - Approach and structure - How

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System Design Mock Interview: Design

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~~Binary and Transistors Explained~~. From a
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~~What I learned in Digital System Design~~
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~~Finite State Machines~~ Digital Design

\u0026amp; Comp. Arch. - Lecture 6:

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Embedded System Design -- A Unified
Hardware/Software Introduction By Frank
Vahid and Tony Givargis, published by J.
Wiley and Sons, (c) 2002. Emphasizes top-
down design involving tradeoffs between
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memory technologies and approaches to
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Frank Vahid is the author of Digital Design with RTL Design, VHDL, and Verilog, 2nd Edition, published by Wiley.

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Vahid Converting to Boolean Equations □
Q1. A fire sprinkler system should spray
water if high heat is sensed and the system
is set to enabled. □ Answer: Let Boolean
variable h represent □high heat is sensed,□

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e represent "enabled," and F represent
"spraying water." Then an equation is: $F =$
h AND e.

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Lysecky, Roman] on Amazon.com.
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Verilog for Digital Design provides a straightforward practical introduction to the use of the Verilog hardware description language for designing digital systems. The book's chapters cover increasingly complex digital design levels, starting with combinational logic, then

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Sequential logic, datapath components, and finally register-transfer ...

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Vahid Step 1: Create a High-Level State Machine

- Let's consider each step of the RTL design process in more detail
- Step 1
- Soda dispenser example
- Not an FSM because:
 - Multi-bit (data) inputs a and s
 - Local register tot
 - Data operations $tot=0$, $tot<s$, $tot=tot+a$.
- Useful high-level state machine:

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of single-purpose processors ("hardware")
and general-purpose processors
("software"), describes memories and
buses, illustrates hardware/software
tradeoffs using a digital camera example,
and ...

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